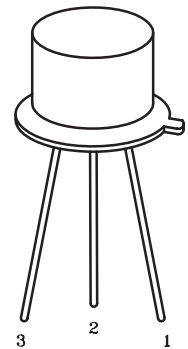
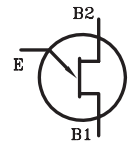


Description:

A TO-18, PN, Unijunction Transistor designed for use in pulse and timing circuits, sensing circuits, and thyristor trigger circuits.

**RoHS
Compliant**



1. EMITTER
2. BASE 1
3. BASE 2

Features:

- Low peak point current : 2 μ A (Max.)
- Low emitter reverse current : 200nA (Max.)
- Passivated surface for reliability and uniformity

Absolute Maximum Ratings: (Ta = 25°C Unless otherwise specified)

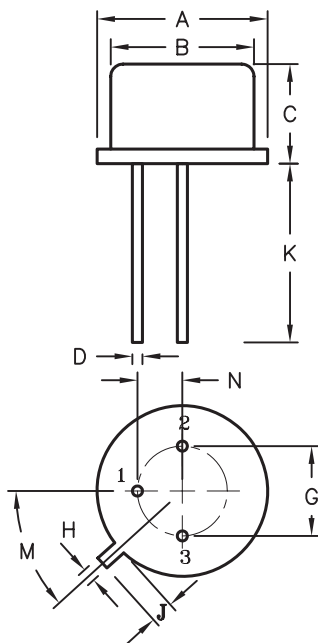
Characteristic	Symbol	Rating
Power Dissipation (Note 1)	P _D	300mW
RMS Emitter Current	I _{E(RMS)}	50mW
Peak Pulse Emitter Current (Note 2)	I _E	2 Amps
Emitter Reverse Voltage	V _{B2E}	30V
Interbase Voltage	V _{B2B1}	35V
Operating Junction Temperature Range	T _J	-65°C to +125°C
Storage Temperature Range	T _{STG}	-65°C to +150°C

Electrical Characteristics: (T_A = +25°C Unless otherwise specified)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit.
OFF Characteristics						
Intrinsic Standoff Ratio	-	V _{B2B1} = 10V, (Note 3)	0.56	-	0.75	-
Interbase Resistance	R _{BB}	V _{B2B1} = 3V, I _E = 0	4.7	7	9.1	k Ω
Interbase Resistance Temperature Coefficient	-	-	0.1	-	0.9	% / °C
Emitter Saturation Voltage	V _{EB1(SAT)}	V _{B2B1} = 10V, I _E = 50mA, (Note 4)	-	3.5	-	V
Modulated Interbase current	V _{B2(MOD)}	V _{B2B1} = 10V, I _E = 50mA	-	15	-	mA
Emitter Reverse Current	I _{EB2O}	V _{B2E} = 30V, I _{B1} = 0	-	0.005	12	μ A
Peak Point Emitter Current	I _P	V _{B2B1} = 25V	-	1	5	μ A
Valley Point Current	I _V	V _{B2B1} = 20V, R _{B2} = 100 Ω	4	6	-	mA
Base-One Peak Pulse Voltage	V _{OB1}	-	3	5	-	V

Notes:

1. Derate 3mW/°C increase in ambient temperature. The total power dissipation (available power to Emitter and Base-Tow) must be limited by the external circuitry.
2. Capacitor discharge - 10µF or less, 30V or less.
3. Intrinsic standoff ration is defined by the equation : $V_P - V_F / V_{B2B1}$
Where : V_P = Peak Point Emitter Voltage; V_{B2B1} = Interbase ; V_F = Emitter to Base-one Junction Diode Drop (~0.45V @ 10µA)
4. Use pulse techniques : Pulse Width ~300µS, Duty Cycle \leq 2% to avoid internal heating due to interbase modulation which may result in erroneous readings.



1. EMITTER
2. BASE 1
3. BASE 2

Dim.	A	B	C	D	G	H	J	K	M	N
Min.	5.31	4.52	4.32	0.41	2.54	0.91	0.71	12.7	45°	1.27
Max.	5.84	4.95	5.33	0.48		1.17	1.22			

Dimensions : Millimetres

Part Number Table

Description	Part Number
Unijunction Transistor, PN, 2A, TO-18	2N2646

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